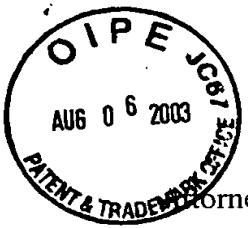


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**RESPONSE UNDER 37 C.F.R. 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2822**

Attorney Docket No. 5649-877

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Cho et al.

Serial No.: 09/923,670

Filed: August 7, 2001

For: INTEGRATED CIRCUIT DEVICES HAVING DIELECTRIC REGIONS
PROTECTED WITH MULTI-LAYER INSULATION STRUCTURES AND
METHODS OF FABRICATING SAME

Group Art Unit: 2836

Examiner: M. Lewis

Confirmation No.: 1538

Date: August 4, 2003

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

13# /amdtC(16)

8/19/03
Open

**AMENDMENT AFTER FINAL ACTION
PURSUANT TO 37 C.F.R. § 1.116**

Sir:

This Amendment is responsive to the Final Office Action of June 4, 2003. Please amend the above-identified application as follows.

If any extension of time for the accompanying response or submission is required, Applicant requests that this be considered a petition therefor. The Commissioner is hereby authorized to charge any additional fee, which may be required, or credit any refund, to our Deposit Account No. 50-0220.

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